



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICATION      Wolfgang Clemens et al.  
OF:  
SERIAL NO:      10/523,487      GROUP ART      3709  
UNIT:  
FILED:      August 26, 2005      EXAMINER:      Joshua P. Wert  
:      CUSTOMER NO.      27162  
FOR:      Electronic Device  
ATTY/DKT NO.:      411000-124

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**DISCLOSURE STATEMENT UNDER 37 CFR 1.56**

SIR:

This paper is to bring to the attention of the PTO the following U.S. copending applications, all of which are related in different respects to organic electronic devices and/or method of making such devices such as transistors, diodes, integrated circuits and the like and/or related technologies. Many of these applications and the instant above-entitled application may have one or more common inventors.

This paper is submitted in compliance with MPEP 2001.06(b) and 2004. Paper copies of these applications as well as copies of applicants' copending applications cited in prior submitted Disclosure Statements are not required under MPEP 609.04(a) II and (c), wherein the requirement to submit copies to the PTO is waived. See the cited 1287 O.G. 163 (10/19/04).

The enclosed PTO 1449 lists related applications noted below. It is respectfully requested that the Examiner consider and make of record all of the cited applications listed

in the enclosed PTO 1449 and in all prior submitted similar Disclosure Statements by initialing each item and returning a copy of the initialed paper to applicants.

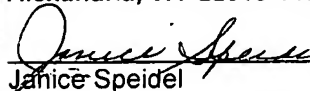
Serial No.	Title	Inventors	Our Ref.
10/562,989	Method and Device for Patterning Organic Layers	Jurgen Ficker et al.	411000-143
10/562,869	Logic Gate With A Potential-Free Gage Electrode for Organic Integrated Circuits	Wolfram Glauert et al.	411000-144
10/569,763	Organic Electronic Component With High Resolution Structuring And Method For The Production Thereof	Walter Fix et al.	411000-146
10/568,730	Organic Capacitor With Voltage-Controlled Capacitance	Wolfgang Clemens et al.	411000-147
10/569,233	Polymer Mixtures for Printed Electronic Circuits	Adolf Bernds et al.	411000-148
10/570,571	Mechanical Control Elements for Organic Polymer Electronic Devices	Wolfgang Clemens et al.	411000-149
10/585,775	Organic Transistor Comprising a Self-aligning Gate Electrode and Method for the Production Thereof	Walter Fix et al.	411000-150
11/574,139	External Package Capable of Being Radio-Tagged	Jurgen Ficker et al.	411000-153

The Commissioner is authorized to charge payment of any fees associated with this communication or credit any overpayment to Deposit Account No. 03-0678.

**FIRST CLASS CERTIFICATE**

I hereby certify that this correspondence is being deposited on the date set forth below with the U.S. Postal Service as First Class Mail, postage prepaid, in an envelope addressed to:

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 May 21, 2007  
Janice Speidel Date

Respectfully submitted,  
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***Complete if Known***

**(Use as many sheets as necessary)**

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Application Number	10/523,487
Filing Date	August 26, 2005
First Named Inventor	Wolfgang Clemens
Group Art Unit	3709
Examiner Name	Joshua P. Wert
Attorney Docket Number	411000-124

[illegible]**Date Considered**

**#318486**